

## Electronic Patent Application Fee Transmittal

Application Number:	10053707			
Filing Date:	24-Jan-2002			
Title of Invention:	Instruction processing method for verifying basic instruction arrangement in VLIW instruction for variable length VLIW processor			
First Named Inventor/Applicant Name:	Teruhiko Kamigata			
Filer:	John Russell Bednarz/Michelle Spahic			
Attorney Docket Number:	1614.1210			

Filed as Large Entity

### Utility under 35 USC 111(a) Filing Fees

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
-------------	----------	----------	--------	----------------------

Basic Filing:

Pages:

Claims:

Miscellaneous-Filing:

Petition:

Patent-Appeals-and-Interference:

Post-Allowance-and-Post-Issuance:

Extension-of-Time:

Extension - 1 month with \$0 paid	1251	1	130	130
-----------------------------------	------	---	-----	-----

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Miscellaneous:</b>				
Request for continued examination	1801	1	810	810
<b>Total in USD (\$)</b>				<b>940</b>